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DATE:

September 11, 2003

TO:

Thiem D. Phan - Art Unit 3729

COMPANY:

USPTO

PHONE:

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FROM:

John D. Goodhue

NUMBER OF PAGES (Including cover): 6

COMMENTS: Please file the attached AMENDMENT:

APPLICANT:

HUBER et al.

SERIAL NO:

10/091,792

FILED

March 6, 2002

TITLE

METHOD FOR MANUFACTURING A POWER CHIP

RESISTOR

Grp./A.U.

3729

Examiner

PHAN, Thiem D.

Conf. No.

9696

Docket No.

P04870US1

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#5/Amendment

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**APPLICANT** 

HUBER, Louis P.

SERIAL NO

10/091,792

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METHOD FOR MANUFACTURING A POWER CHIP RESISTOR

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**AMENDMENT** 

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Commissioner for Patents P.O. Box 1450 Mail Stop Non-Fee Amendment Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated June 12, 2003, kindly consider the following amendments and remarks relative to reconsideration of the present application.

CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8(a))

I hereby certify that this correspondence is, on the date shown below, being:

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□ deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Mail Stop Non-Fee Amendment, Alexandria, VA 22313-1450.

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IOHN D. GOODHILE